

**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

APPL. NUM.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10083533	02/27/2002	438	315 257	2812	Thao Le W. Peckay

**\*\*APPLICANTS:** Hashimoto Hiroshi; Takahashi Koji

**CONTINUING DATA VERIFIED:** No

TL

**\*\* FOREIGN APPLICATIONS VERIFIED:** Yes

JAPAN 2001-205188 07/05/2001

TL

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO.
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	020244
Verified and Acknowledged Examiner's initials		
TITLE : Semiconductor integrated circuit device and method of producing the same		
U.S. DEPT. OF COMM/USPTO-105 (Rev. 12-94)		

NAME OF ALLOWANCE MAILED		CD'S OR ALL COPIES	
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drawn:	Fig's Drawn:
Amount Due	Date Paid		Print Fig.
TERMINAL		Primary Examiner	
DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
WARNING: The information disclosed herewith may be restricted. Unauthorized disclosure may be prohibited by the United States Code, Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent and Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

DISK (CRF)

CD-ROM  
(Attached in pocket on front inside cover)